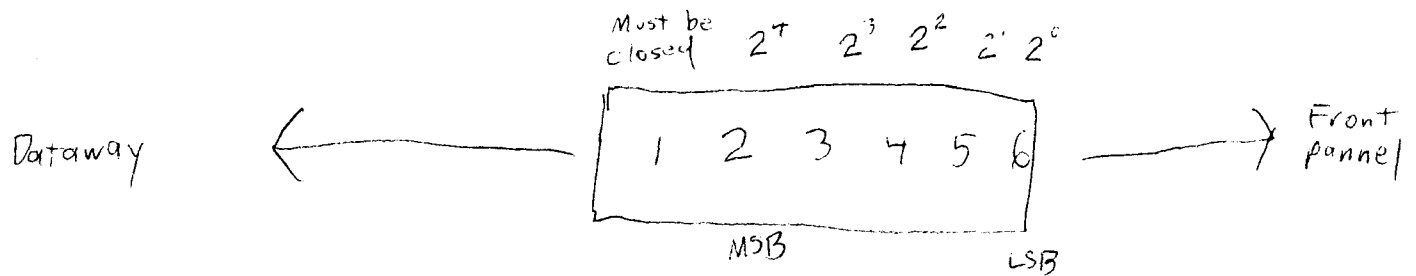


908 Memory Switches

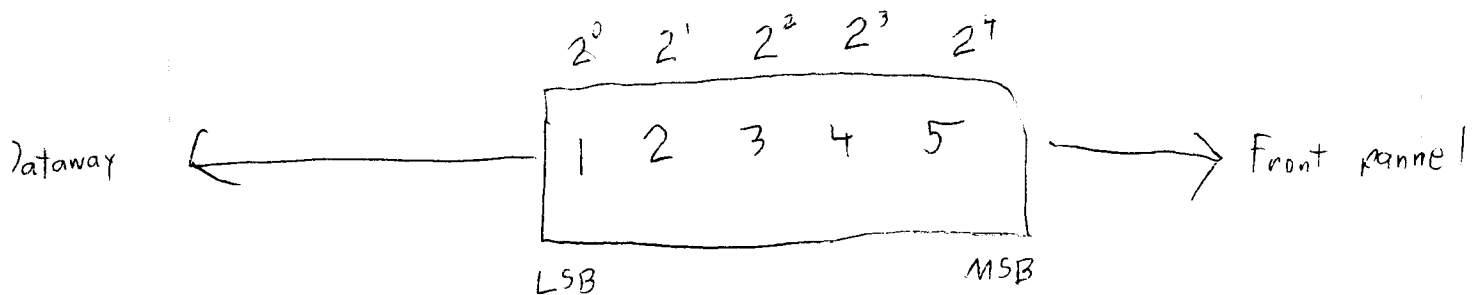
Closed = 0



one + number represented by switches = number of 32K memory modules.

903 Memory Switches

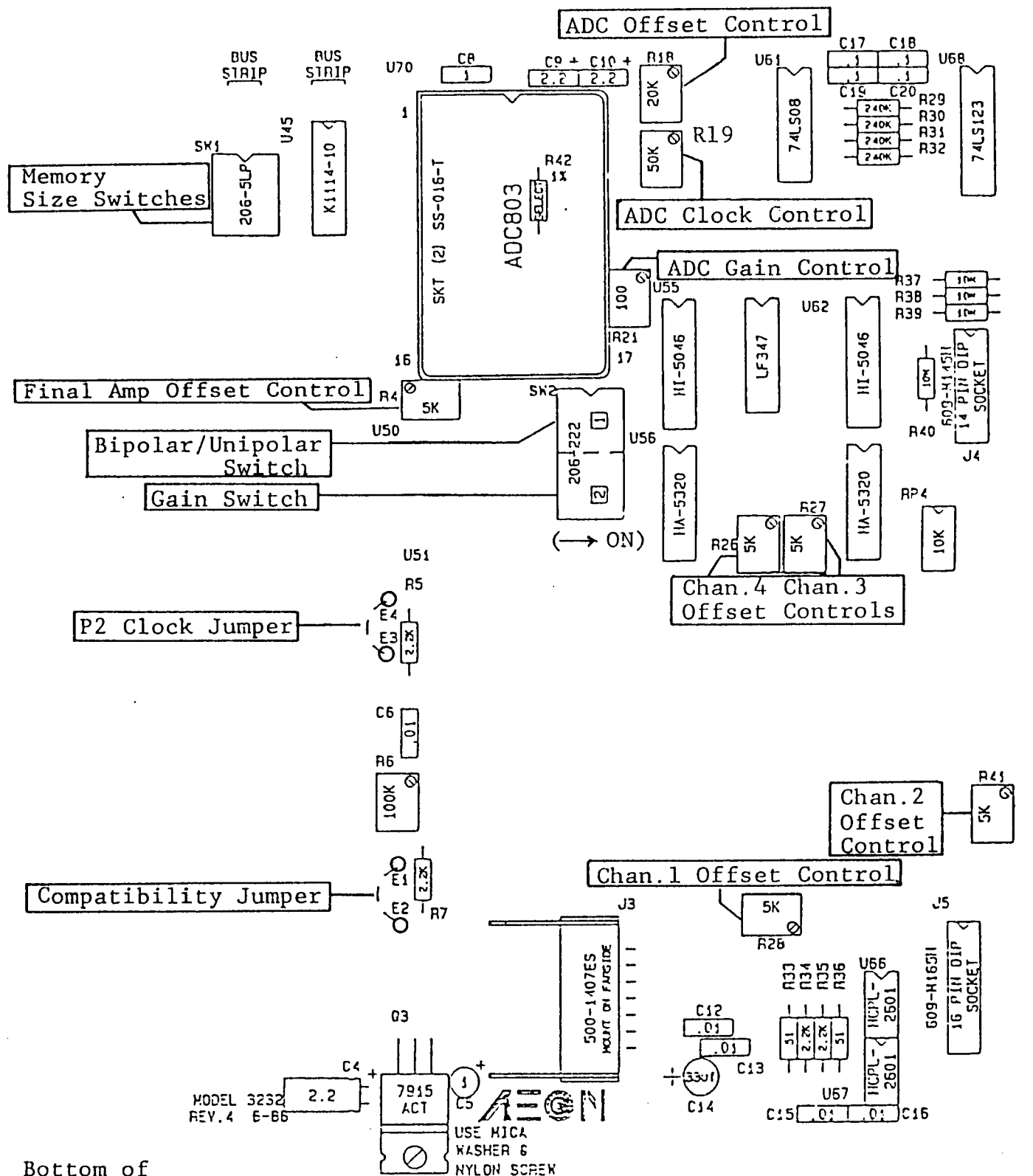
Closed = 0



number represented by switches = number of 32K modules or below this one, or
 32K X number represented by switches = starting address of module

3232 Calibration Pot Locations

Top of Control (left) board



Bottom of Control (left) board

(Some components deleted for clarity)

Figure 2-1. Switch, Jumper and Controls Positions

3232 Switch Settings

B. Memory Size

The size of available memory is set by the five dip switches located on the side of the module. The switches are set as follows:

Switch 1	Switch 2	Switch 3	Switch 4	Switch 5	Memory Size
ON	ON	ON	ON	ON	32K
ON	ON	ON	ON	OFF	64K
ON	ON	ON	OFF	ON	96K
ON	ON	ON	OFF	OFF	128K
ON	ON	OFF	ON	ON	160K
ON	ON	OFF	ON	OFF	192K
ON	ON	OFF	OFF	ON	224K
ON	ON	OFF	OFF	OFF	256K
ON	OFF	ON	ON	ON	288K
ON	OFF	ON	ON	OFF	320K
ON	OFF	ON	OFF	ON	352K
ON	OFF	ON	OFF	OFF	384K
ON	OFF	OFF	ON	ON	416K
ON	OFF	OFF	ON	OFF	448K
ON	OFF	OFF	OFF	ON	480K
ON	OFF	OFF	OFF	OFF	512K
OFF	ON	ON	ON	ON	544K
OFF	ON	ON	ON	OFF	576K
OFF	ON	ON	OFF	ON	608K
OFF	ON	ON	OFF	OFF	640K
OFF	ON	OFF	ON	ON	672K
OFF	ON	OFF	ON	OFF	704K
OFF	ON	OFF	OFF	ON	736K
OFF	ON	OFF	OFF	OFF	768K
OFF	OFF	ON	ON	ON	800K
OFF	OFF	ON	ON	OFF	832K
OFF	OFF	ON	OFF	ON	864K
OFF	OFF	ON	OFF	OFF	896K
OFF	OFF	OFF	ON	ON	928K
OFF	OFF	OFF	ON	OFF	960K
OFF	OFF	OFF	OFF	ON	992K
OFF	OFF	OFF	OFF	OFF	1024K

Input range

0 to +10.24 Volts
 -10.24 to +10.24 Volts
 0 to +5.12 Volts
 -5.12 to +5.12 Volts

Switch 1

OFF
 ON
 OFF
 ON

Switch 2

OFF
 OFF
 ON
 ON

A18 offset



A19 ADC clock



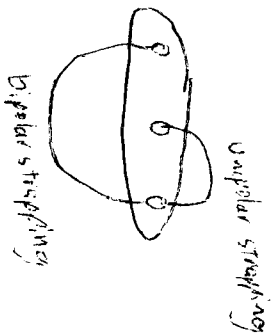
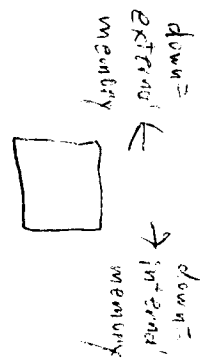
A21 Gain



907 Switches and Pots

907C

- ☐ R17 gain for $\pm 10V$ only
- ☐ R19 gain for all other ranges
- ☐ R20 offset
- ☐ R26 ADC Clock



gain $0 = 10V$
 $0.4 = 5V$
☐ ☐ ☐ ☐
 on = unipolar
 off = bipolar

☐ R3 gain for all ranges except 50

☐ R2 $\pm 10V$ gain

☐ R4 offset

☐ R1 ADC clock

Channel Select
 $2^0 2^1 2^2 2^3$ (on = 1)

LSB MSB

912/914 Switch Settings

10-20-86

appendix B
 "YELLOW DOT" SWITCH SETTINGS
 912/914
 ↳ 2 MHz
 ↳ 500 KHz

	on	off
<u>nu</u>	1	
<u>nu</u>	2	
<u>delay control</u>	3	
<u>delay disable</u>	4	
<u>mem1</u>	5	
<u>mem0</u>	6	
<u>H912/914</u>	7	

BA32

	8k	32k	64k	128k
mem1	on	on	off	off
mem0	on	off	on	off

	912	914
H912/914	on	off

delay control

	sw3	sw4	mode
invalid	on	on	not used
software control	on	off	master(1)/slave(0)
hardware control	off	on	delay disabled (slave)
hardddware control	off	off	delay enabled (master)